EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S122	81	processor with (instruction near cache) and S121	US-PGPUB; USPAT; USOCR	OR	ON	2008/03/17 15:06
S121	2122	717/127-133.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/17 15:06
S120	4869	processor with (instruction near cache)	US-PGPUB; USPAT; USOCR	OR	ON	2008/03/17 15:06
S119	26	system with on with chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/17 13:43
S118	0	system adj on adj chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/17 13:42
S117	2	"20040030870".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/17 12:50
S116	26	S113 and S115	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/16 15:16
S115	2122	717/127-133.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/16 15:16
S114	171	S113 and "10"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/16 15:15
S113	182	(circuit processor) with (debug$5 monitor$4) with (instruction adj cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/16 15:15

| S112 | 261397 | (circuit processor) with (debug$5 monitor$4) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/03/16 15:15 |

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